# **PQ1CG3032FZ**/ PQ1CG3032RZ

#### Features

- 1. Maximum switching current:3.5A
- 2. Built-in ON/OFF control function
- 3. Built-in soft start function to suppress overshoot of output voltage in power on sequence or ON/OFF control sequence
- 4. Built-in oscillation circuit (Oscillation frequency:TYP. 150kHz)
- 5. Built-in overheat/overcurrent protection function
- 6. TO-220 package
- 7. Variable output voltage

(Output variable range: Vref to 35V/-Vref to -30V) [Possible to select step-down output/inversing output according to external connection circuit]

8. PQ1CG3032FZ:Zigzag forming PQ1CG3032RZ:Self-stand forming

#### Applications

- 1. CTV
- 2. Digital OA equipment
- 3. Facsimiles, printers and other OA equipment
- 4. Personal computers and amusement equipment

#### Absolute Maximum Ratings $(Ta=25^{\circ}C)$ Parameter Symbol Rating Unit <sup>1</sup>Input voltage VIN 40 V VADJ 7 V Output adjustment terminal voltage VI-0 41 V Dropout voltage v <sup>\*2</sup>Output-COM voltage VOUT -1\*3 ON/OFF control voltage Vc v -0.3 to +403.5 А Isw Switching current 1.4 W P<sub>D1</sub> \*4 Power dissipation PD2 14 W \*5 Junction temperature Ti 150 °C Topr -20 to +80°C Operating temperature Tstg -40 to +150 °C Storage temperature <sup>6</sup>Soldering temperature 260 °C Tsol

\*1 Voltage between VIN terminal and COM terminal

\*2 Voltage between VOUT terminal and COM terminal \*3 Voltage between ON/OFF control and COM terminal

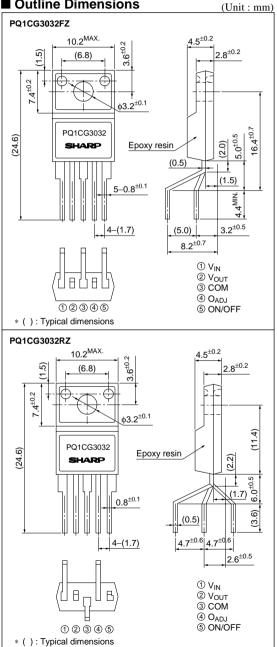
\*4 PD:With infinite heat sink

\*5 Over heat protection may operate at the condition Ti=125°C to 150°C

\*6 For 10s

## **TO-220 Type Chopper Regulator**

#### Outline Dimensions



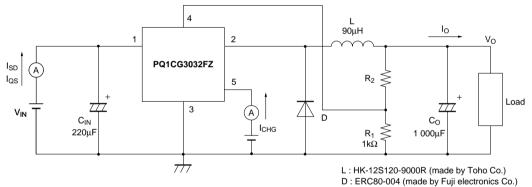
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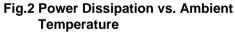
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#### PQ1CG3032FZ/PQ1CG3032RZ

Electrical Characteristics (Unless otherwise specified, condition shall be VIN=12V, Io=0.5A, Vo=5V, ON-OFF terminals is open, Ta=25°C						
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output saturation voltage	VSAT	Isw=3A	-	1.4	1.8	V
Reference voltage	Vref	_	1.235	1.26	1.285	V
Reference voltage temperature fluctuation	$\Delta V_{ref}$	Tj=0 to 125°C	-	±0.5	-	%
Load regulation	RegL	Io=0.5 to 3A	-	0.2	1.5	%
Line regulation	RegI	VIN=8 to 35V	-	1	2.5	%
Efficiency	η	Io=3A	-	80	_	%
Oscillation frequency	fo	_	135	150	165	kHz
Oscillation frequency temperature fluctuation	Δfo	Tj=0 to 125°C	-	±2	-	%
Overcurrent detecting level	IL	-	3.6	4.7	5.8	Α
Charge current	Існд	2, 4 terminals is open, 5 terminal	-	-10	-	μΑ
Threshold input voltage	VTHL	Duty ratio=0%, (4) terminal=0V, (5) terminal	-	1.3	-	V
	VTHH	Duty ratio=100%, (4) terminals is open, (5) terminal	-	2.3	-	V
ON threshold voltage	VTH(ON)	(4) terminal=0V, (5) terminal	0.7	0.8	0.9	V
Stand-by current	Isd	VIN=40V, (5) terminal=0V	-	140	400	μΑ
Output OFF-state consumption current	Iqs	VIN=40V, (5) terminal=0.9V	-	8	16	mA

### Fig.1 Standard Test Circuit





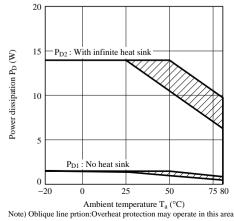
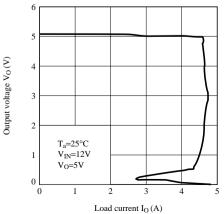


Fig.3 Overcurrent Protection Characteristics (Typical Value)



#### Fig.4 Efficiency vs. Input Voltage

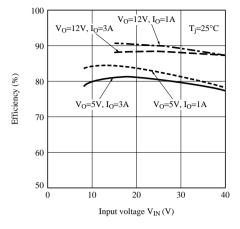


Fig.6 Stand by Current vs. Intput Voltage

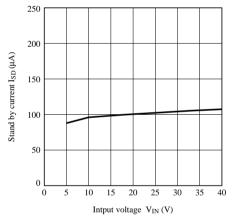


Fig.8 Load Regulation vs. Output Current

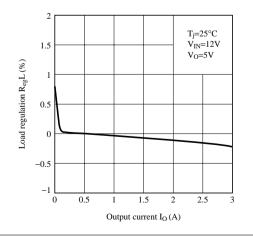


Fig.5 Output Saturation Voltage vs. Switching Current

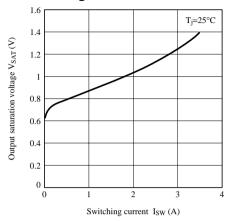


Fig.7 Reference Voltage Fluctuation vs. Junction Temperature

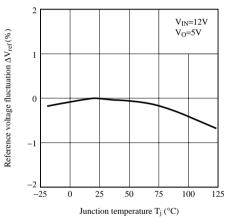


Fig.9 Line Regulation vs. Input Voltage

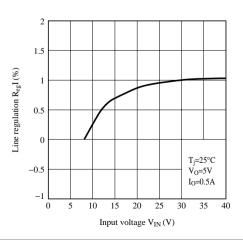
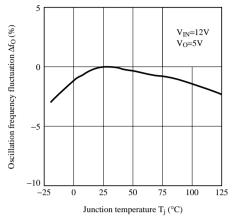
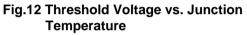
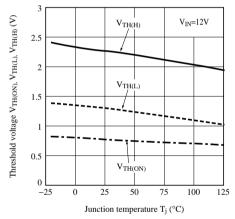


Fig.10 Oscillation Frequency Fluctuation vs. Junction Temperature







#### Fig.14 Block Diagram

Fig.11 Overcurrent Detection Level Fluctuation vs. Junction Temperature

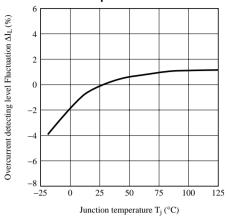
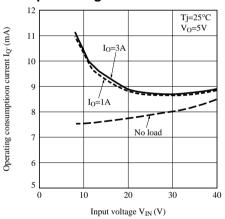
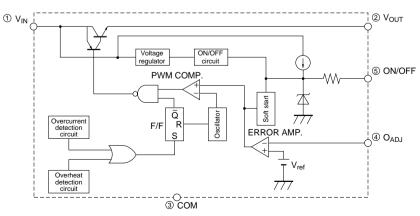


Fig.13 Operating Consumption Current vs. Input Voltage





#### Fig.15 Step Down Type Circuit Diagram

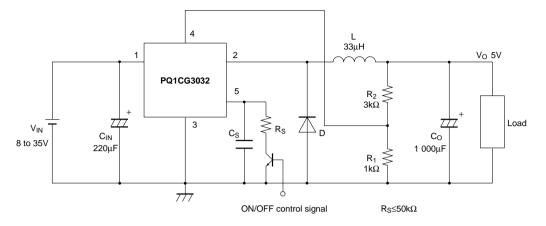
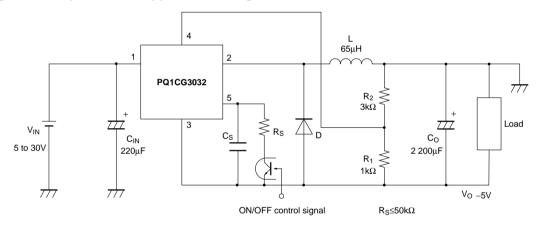
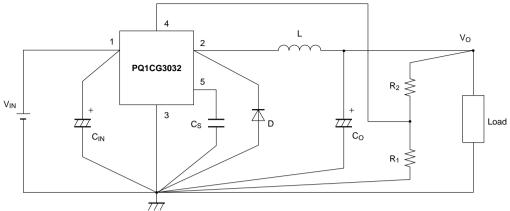


Fig.16 Polarity Inversion Type Circuit Diagram

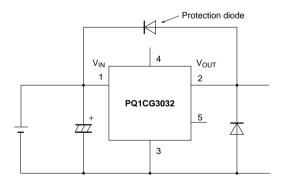


# Precautions for Use

SUADD



- 1. External connection
  - (1) Wiring condition is very important. Noise associated with wiring inductance may cause problems. For minimizing inductance, it is recommended to design the thick and short pattern (between large current diodos, input/output capacitors, and terminal 1,2.) Single-point grounding (as indicated) should be used for best results.
  - (2) High switching speed and low forward voltage type schottky barrier diode should be recommended for the catch-diode D because it affects the efficiency. Please select the diode which the current rating is at least 1.2 times greater than maximum swiching current.
  - (3) The output ripple voltage is highly influenced by ESR (Equivalent Series Resistor) of output capacitor, and can be minimized by selecting Low ESR capacitor.
  - (4) An inductor should not be operated beyond its maximum rated current so that it may not saturate.
  - (5) When voltage that is higher than  $V_{IN}$  (1), is applied to  $V_{OUT}$  (2), there is the case that the device is broken. Especially, in case  $V_{IN}$  (1) is shorted to GND in normal condition, there is the case that the device is broken since the charged electric charge in output capacitor (C<sub>0</sub>) flows into input side. In such case a schottly barrier diode or a silicon diode shall be recommended to connect as the following circuit.



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#### ON/OFF Control Terminal

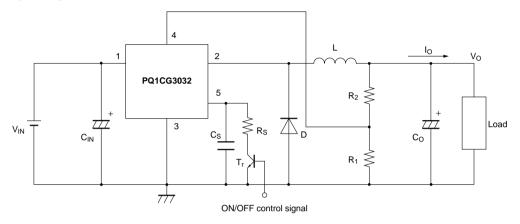
1. In the following circuit, when ON/OFF control terminal (5) becomes low by switching transistor Tr on, output voltage may be turned OFF and the device becomes stand-by mode. Dissipation current at stand-by mode becomes Max.400µA.

2. Soft start

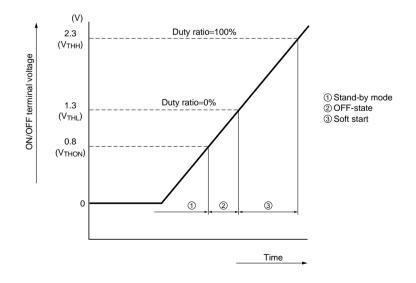
When capacitor Cs is attached, output pulse gradually expanded and output voltage will start softly.

3. ON/OFF control with soft startup

For ON/OFF control with capacitor  $C_s$ , be careful not to destroy a transistor Tr by discharge current from  $C_s$ , adding a resistor restricting discharge current of  $C_s$ .



#### ON-OFF Terminal Voltage vs. Time



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